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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/511,620	02/23/2000	Tomomi Momohara	005702-20068	3356

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LOS ANGELES, CA 90071-2611

EXAMINER

FENTY, JESSE A

ART UNIT	PAPER NUMBER
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2815

DATE MAILED: 12/20/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/511,620

Applicant(s)

MOMOHARA, TOMOMI

Examiner

Jesse A. Fenty

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 06 September 2002.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 21,28,35 and 41-67 is/are pending in the application.
- 4a) Of the above claim(s) 56-67 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 41-43,45-47 and 49-54 is/are rejected.
- 7) ☒ Claim(s) 44,48 and 55 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

### Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Election/Restrictions*

1. Newly submitted claims 56-67 are directed to an invention that is independent or distinct from the invention originally claimed for the following reasons: said claims are directed to a device and method of testing a complex semiconductor device

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution on the merits. Accordingly, claims 56-67 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

### *Claim Rejections - 35 USC § 102*

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 41-43, 45-47 and 49-54 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamamoto (U.S. Patent No. 5,239,197).

In re claim 41, Yamamoto discloses a semiconductor device, comprising:

A semiconductor substrate (21) of a first conductivity type;

At least two first well regions (24, 22) of a second conductivity type formed in the semiconductor substrate;

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At least one second well region (25) of the first conductivity type formed in the at least two first well regions; and

Semiconductor MOSFET elements formed in the at least two first well regions and the at least one second well region,

Wherein a memory circuit comprises the semiconductor MOSFET elements.

In re claim 45, Yamamoto discloses the device of claim 41, wherein a potential supplied to the at least two first well regions differs from a potential supplied to the at least one second well region.

In re claim 42, Yamamoto discloses a semiconductor device, comprising:

A semiconductor substrate (21) of a first conductivity type;

At least two first well regions (24, 22) of a second conductivity type formed in the semiconductor substrate; and

Integrated circuits which are formed on the at least two first well regions respectively, and which have different functions.

In re claim 43, Yamamoto discloses the device of claim 42, wherein a second well region (25) of the first conductivity type is formed in at least one of the at least two first well regions, and one of the integrated circuits is formed on the at least one of the at least two first well regions and the second well region.

In re claim 46, Yamamoto discloses the device of claim 42, wherein a potential is supplied to the at least two first well regions.

In re claim 47, Yamamoto discloses the device of claim 43, wherein a potential supplied to the at least two first well regions differs from a potential supplied to the second well region.

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In re claims 49-54, Yamamoto discloses the device of claim 42. The limitations regarding the use of the semiconductor circuit are intended use limitations. Terms that simply set forth the intended use, a property inherent in or a function, do not differentiate the claimed composition of these elements from those known to prior art.

***Response to Arguments***

4. Applicant's arguments with respect to the new claims have been considered but are moot in view of the new ground(s) of rejection.

***Allowable Subject Matter***

5. Claims 21, 28 and 35 are allowed.

6. Claims 44, 48 and 55 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

a. In re claim 44, the semiconductor device including at least a second well region of the first conductivity type having a third well region of the second conductivity type formed therein is neither anticipated nor obvious over the prior art of record.

***Conclusion***

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jesse A. Fenty whose telephone number is 703-308-8137. The examiner can normally be reached on 5/4-9 1st Fri. Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on 703-308-1690. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-746-3892 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

JAF  
December 16, 2002

Jesse A. Fenty  
Examiner  
Art Unit 2815

EDDIE LEE  
SUPERINTENDING PATENT EXAMINER  
TECHNOLOGY CENTER 8000